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IN RE APPLICATION OF: KIM, ET AL.,

APPLICATION NO.: 09/989,647

FILED: NOVEMBER 20, 2001

GROUP ART UNIT: 2634

CONF. NO. 1014

EXAMINER: J. JOSEPH

For: LOGIC GATES INCLUDING DIODE-CONNECTED METAL-OXIDE-SEMICONDUCTOR FIELD-EFFECT TRANSISTOR (MOSFETS) TO CONTROL INPUT THRESHOLD VOLTAGE LEVELS AND SWITCHING TRANSIENTS OF OUTPUT LOGIC SIGNALS

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Date: June 10, 2005By: Maureen Golob

Maureen Golob

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Attorney Docket No. 59472-8822.US00

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Maureen Golob

PATENT

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Sir:

Effective immediately, please direct all further communications in the above-identified patent application to the following address:

Paul L. Hickman  
Perkins Coie LLP  
P. O. Box 2168  
Menlo Park, CA 95026-2168

Respectfully submitted,  
Perkins Coie LLP

Date: June 10, 2005

Judy Mohr  
Judy Mohr  
Registration No. 38,563

Correspondence Address:

Customer No. 22918  
Perkins Coie LLP  
P. O. Box 2168  
Menlo Park, California 94026-2168  
(650) 838-4300

BY051600.119